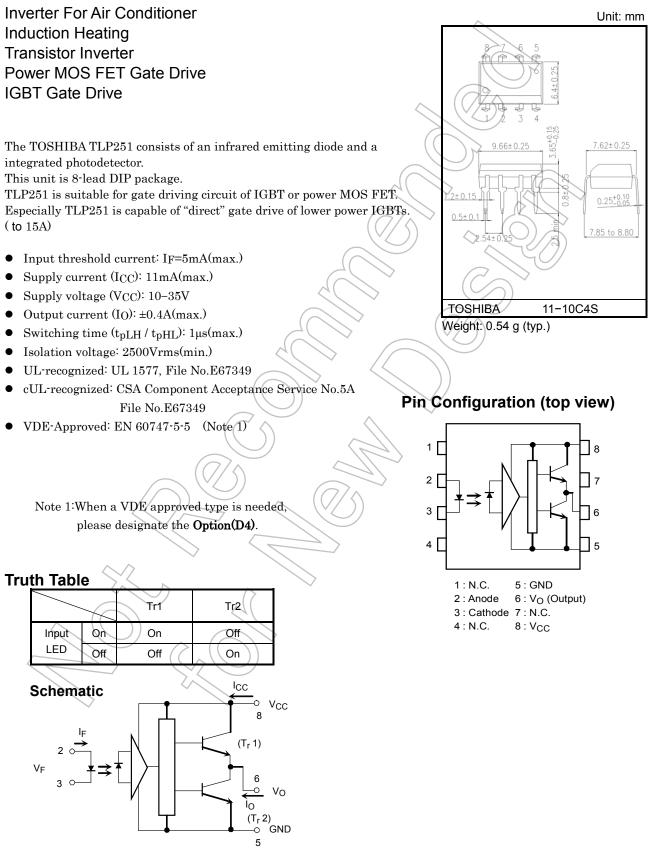
TOSHIBA Photocoupler IRED & Photo-IC

TLP251



A 0.1μ F bypass capcitor must be connected between pin 8 and 5(see Note 5).

TOSHIBA

1992-01

Start of commercial production

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit	
LED	Forward current		lF	20	mA	
	Forward current derating	ΔI _F / ΔTa	- 0.36	mA /°C		
	Peak transient forward current	IFPT	1	A		
	Reverse voltage		VR	5	V	\bigcirc
	Diode power dissipation		PD	40	(mW/ <	
	Diode power dissipation deratir	ng (Ta≥70 °C)	∆PD/°C	-0.72	mW/°C)
	Junction temperature		Tj	125	°C	
	"H" peak output current (P _W ≤ 2.0μs, f ≤ 15kHz)	(Note 2)	Іорн	-0.4	A	
	"L" peak output current (P _W ≤ 2.0μs, f ≤ 15kHz)	(Note 2)	I _{OPL}	0.4	A	
	Output voltage	(Ta ≤ 70°C)	Vo	35	v	R
		(Ta = 85°C)		24	R	$\mathbb{N}^{\mathbb{C}}$
tor	Supply voltage	(Ta ≤ 70°C)	Vcc	35	\mathbf{V}	
Detector		(Ta = 85°C)		24	$\overline{\partial}$	
D	Output voltage derating (Ta ≥ 70°C)	~	ΔVο / ΔΤα	-0.73	V/°C	
	Supply voltage derating (Ta ≥ 70°C)		∆V _{CC} / ∆Та	-0.73) v∕°C	
	Output Power dissipation		Po	800	mW	
	Output Power dissipation derat	ing (Ta ≥70°C)	ΔP _O /°C	-14.5	mW/°C	
	Junction temperature		Тј 🤇	125	°C	
Opera	ating frequency	(Note 3)	f	25	kHz	
Opera	ating temperature range		Topr	-20 to 85	°C	
Stora	ge temperature range	$ \rightarrow $	T _{stg}	-55 to 125	°C	
Lead	soldering temperature(10 s)		T _{sol}	260	°C	
Isolat	tion voltage (AC, 60 s.,R.H.≤ 60	%) (Note 4)	BVs	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width $P_W \le 1 \ \mu s$, 300 pps

Note 2: Expornential waveform

Note 3: Exportential waveform, $I_{OPH} \le -0.25 \text{ A}(\le 2.0 \text{ }\mu\text{s})$, $I_{OPL} \le +0.25 \text{ A}(\le 2.0 \text{ }\mu\text{s})$

Note 4: Device considerd a two terminal device: Pins 1, 2, 3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit	
Input current, on	(Note 1)	I _{F(ON)}	7	8	10	mA
Input voltage, off		VF(OFF)	0	_	0.8	V
Supply voltage		V _{CC}	10	_	30	V
Peak output current		IOPH / IOPL	_	_	±0.1	A
Operating temperature		T _{opr}	-20	25	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 1: Input signal rise time(fall time)<0.5 μ s.

Electrical Characteristics (Ta = -20 to 70°C, unless otherwise specified)

Characteristic		Symbol	Test Cir– cuit	Test Condition	Min.	Тур.*	Max.	Unit
Input forward voltage		VF		I _F = 10 mA , Ta = 25 °C	$\langle \gamma \rangle$	1.6	1.8	V
Temperature coefficient of forward voltage		ΔV _F / ΔTa	- (IF = 10 mA		-2.0	_	mV / °C
Input reverse current		IR	£	V _R = 5 V, Ta = 25 °C	<i>7</i> –	—	10	μA
Input capacitance	Ст	X	V = 0 V , f = 1 MHz , Ta = 25 °C	_	45	250	pF	
Output current	"H" level	Іорн		V _{CC} =30 V	-0.1	-0.25	_	•
	"L" level		2	(Note 1) IF =0 mA V6-5 = 2.5 V	0.1	0.2	—	A
Outent with an	"H" level	Уон	3	V _{CC1} = +15 V, V _{EE1} = -15 V R _L = 200 Ω, I _E = 5 mA	11	13.2	—	v
Output voltage	"L" level	VoL	4	$V_{CC1} = +15 \text{ V}, V_{EE1} = -15 \text{ V}$ RL = 200 Ω , VF = 0.8 V	_	-14.5	-12.5	v
	"H" level	Іссн		V _{CC} = 30 V, I _F = 10 mA Ta = 25 °C	_	7.5	_	
	~		\backslash	V _{CC} = 30 V, I _F = 10 mA	-	—	11	m (
Supply current	"L" level	ICCL	_	V _{CC} = 30 V, I _F = 0 mA Ta = 25 °C	_	8	_	mA
\sim (\bigcirc)				V _{CC} = 30 V, I _F = 0 mA	_	—	11	
Threshould input current	"Output $L \to H$ "	IFLH	_	V _{CC1} = +15 V, V _{EE1} = -15 V R _L = 200 Ω, V _O > 0 V	_	1.2	5	mA
Threshold input voltage	"Output H→L"	VFHL	_	V_{CC1} = +15 V, V_{EE1} = -15 V R _L = 200 Ω, V _O < 0 V	0.8	_	_	V
Supply voltage	Vcc	_		10	—	35	V	
Capacitance (input-output)	Cs	_	Vs = 0 V , f = 1 MHz Ta = 25° C	_	1.0	2.0	pF	
Resistance (input-outp	Rs	_	Vs = 500 V, Ta = 25 °C R.H. ≤ 60 %	1×10 ¹²	10 ¹⁴	_	Ω	

Note : All typical values are at Ta = 25 °C

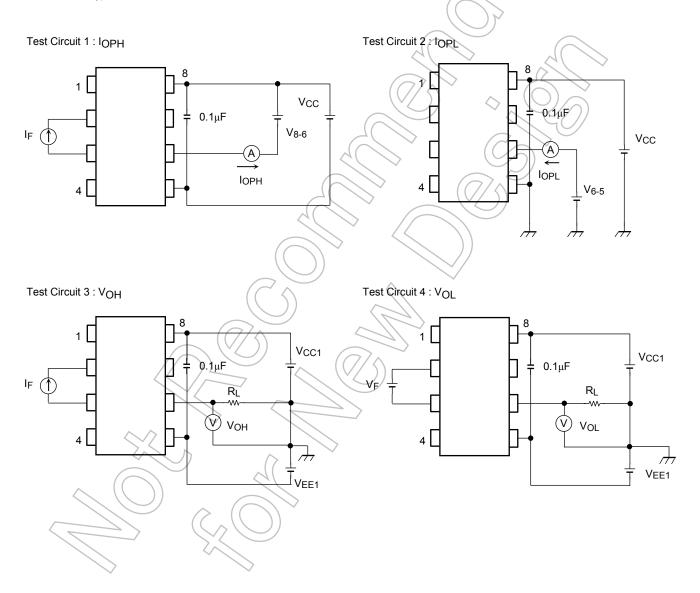
Note 1: Duration of IO time ≤ 50 μ s



Switching Characteristics (Ta = -20 to 70° C, unless otherwise specified)

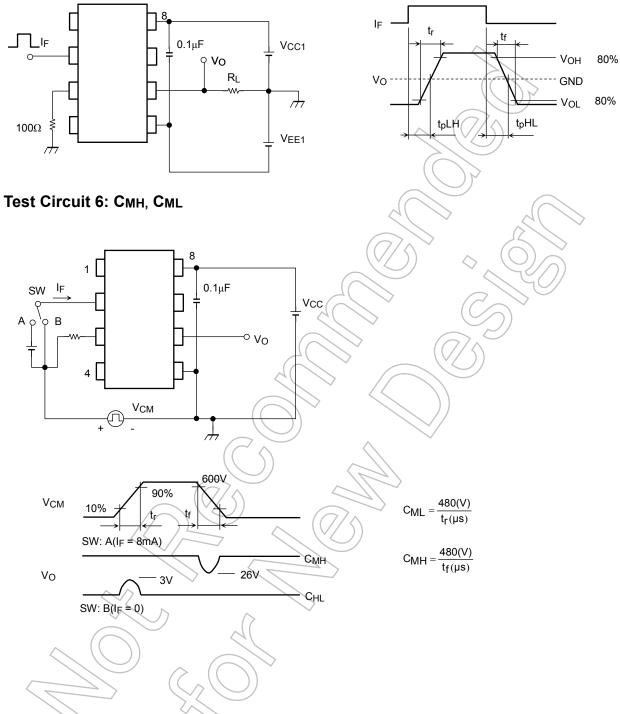
Characteristic		Symbol	Test Cir– cuit	Test Condition	Min.	Тур.*	Max.	Unit
Propagation	L→H	t _{pLH}		IF = 8 mA	—	0.25	1.0	μS
delay time	H→L	tpHL	5	V _{CC1} = +15 V, V _{EE1} = -15 V R _L = 200 Ω	\geq	0.25	1.0	
Common mode transient immunity at high level output		Смн	- 6	V _{CM} = 600 V, I _F = 8 mA, V _{CC} = 30 V, Ta = 25 °C	-5000		_	V / μs
Common mode transient immunity at low level output		C _{ML}	Ū	V _{CM} = 600 V, I _F = 0 mA, V _{CC} = 30 V, Ta = 25 °C	5000	9_	_	V / μs

Note: All typical values are at Ta = 25 °C

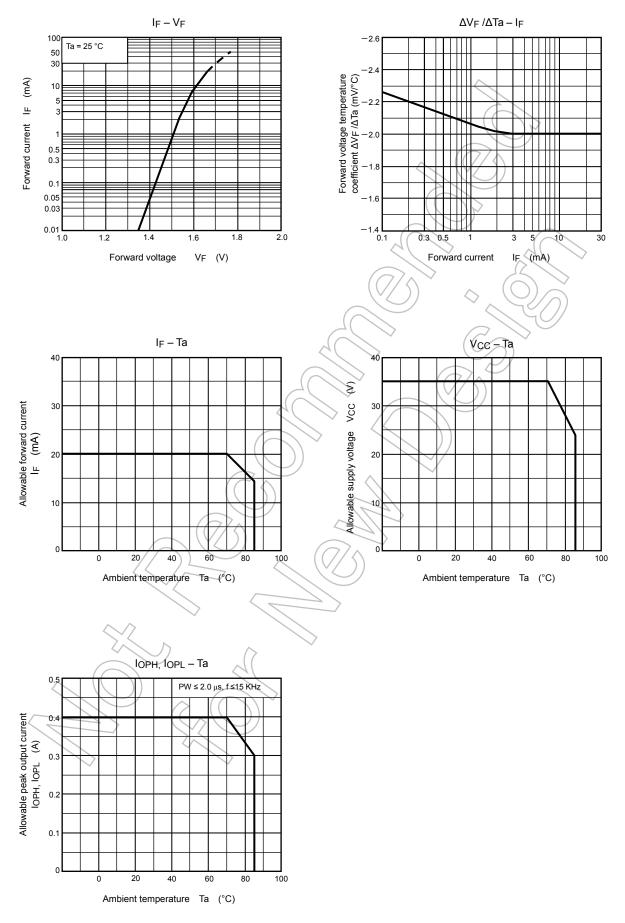


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Test Circuit 5: tpLH, tpHL, tr, tf



 C_{ML} (C_{MH}) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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