

# 2MBI100VA-120-50

IGBT Modules

**Power Module (V series)**  
**1200V / 100A / 2-in-1 package**

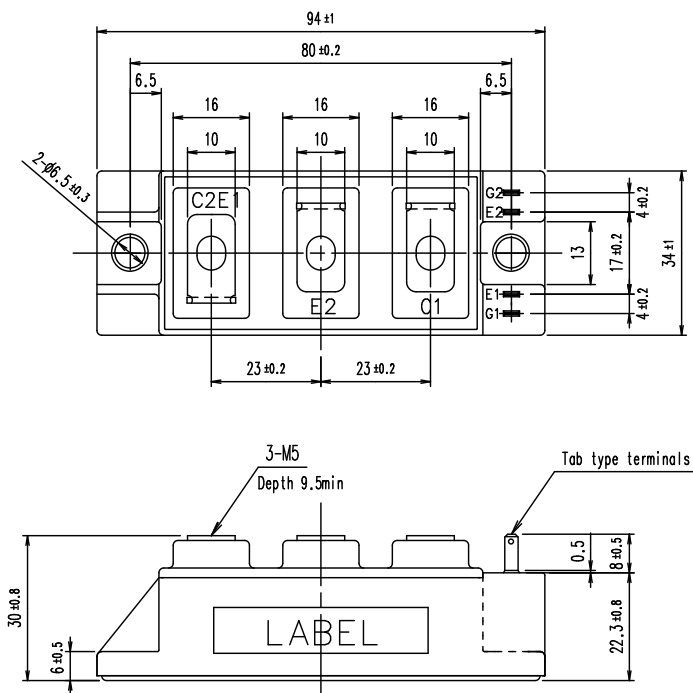
## ■ Features

- AC-switch
- High speed switching
- Voltage drive
- Low Inductance module structure

## ■ Applications

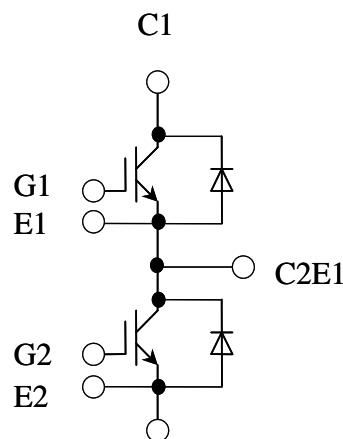
AC-switch for UPS, PCS and etc.

## ■ Outline drawing ( Unit : mm )



Weight: 180g (typ.)

## ■ Equivalent circuit



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■ Absolute maximum ratings (at  $T_c = 25^\circ\text{C}$  unless otherwise specified)

Items		Symbols	Conditions	Maximum ratings	Units
Collector-Emitter voltage		$V_{CES}$		1200	V
Gate-Emitter voltage		$V_{GES}$		$\pm 20$	V
Collector current		$I_C$	Continuous $T_c = 100^\circ\text{C}$	100	A
		$I_C$ pulse	1ms	200	
		$-I_C$		100	
		$-I_C$ pulse	1ms	200	
Collector power dissipation		$P_C$	1 device	555	W
Junction temperature		$T_j$		175	$^\circ\text{C}$
Operating junction temperature (under switching conditions)		$T_{jop}$		150	
Case temperature		$T_c$		125	
Storage temperature		$T_{stg}$		$-40 \sim 125$	
Isolation voltage	Between terminal and copper base (*1)	$V_{iso}$	AC: 1min.	2500	VAC
Screw torque	Mounting	-	M5 or M6	3.0~5.0	N m
	Terminals	-	M5	2.5~5.0	

(\*1) All terminals should be connected together when isolation test will be done.

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## ■ Electrical characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

### NOTICE:

The external gate resistance ( $R_g$ ) shown below is one of our recommend value for the purpose of minimum switching loss. However the optimum  $R_g$  depends on circuit configuration and/or environment. We recommend that the  $R_g$  has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

Items	Symbols	Conditions		Characteristics			Units
				min.	typ.	max.	
Zero gate voltage collector current	$I_{CES}$	$V_{GE}=0V, V_{CE}=1200V$		-	-	1.0	mA
Gate-Emitter leakage current	$I_{GES}$	$V_{CE}=0V, V_{GE}=\pm 20V$		-	-	200	nA
Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE}=20V, I_C=100mA$		6.0	6.5	7.0	V
Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal)	$V_{GE}=15V, I_C=100A$	$T_J=25^{\circ}C$	-	1.90	2.35	V
			$T_J=125^{\circ}C$	-	2.20	-	
			$T_J=150^{\circ}C$	-	2.25	-	
	$V_{CE(sat)}$ (chip)	$V_{GE}=15V, I_C=100A$	$T_J=25^{\circ}C$	-	1.75	2.20	
			$T_J=125^{\circ}C$	-	2.05	-	
			$T_J=150^{\circ}C$	-	2.10	-	
Internal gate resistance	$R_{g(int)}$	-		-	7.5	-	$\Omega$
Input capacitance	$C_{ies}$	$V_{CE}=10V, V_{GE}=0V, f=1MHz$		-	9.1	-	nF
Turn-on time	$t_{on}$	$V_{CC}=600V, I_C=100A, V_{GE}=\pm 15V, R_g=1.6\Omega, T_J=150^{\circ}C, L_s=30nH$		-	600	-	nsec
	$t_r$			-	200	-	
	$t_{r(i)}$			-	50	-	
Turn-off time	$t_{off}$			-	600	-	
	$t_f$			-	40	-	
Forward on voltage	$V_F$ (terminal)	$V_{GE}=0V, I_F=100A$	$T_J=25^{\circ}C$	-	1.80	2.25	V
			$T_J=125^{\circ}C$	-	1.95	-	
			$T_J=150^{\circ}C$	-	1.90	-	
	$V_F$ (chip)	$V_{GE}=0V, I_F=100A$	$T_J=25^{\circ}C$	-	1.70	2.15	
			$T_J=125^{\circ}C$	-	1.85	-	
			$T_J=150^{\circ}C$	-	1.80	-	
Reverse recovery time	$t_{rr}$	$I_F=100A$		-	150	-	nsec

## ■ Thermal resistance characteristics

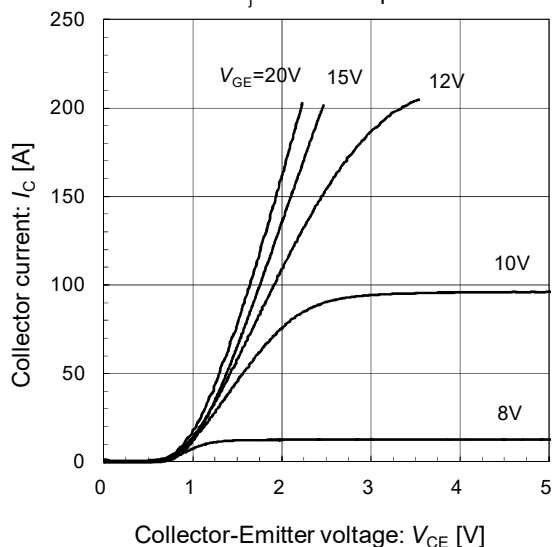
Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance (1device)	$R_{th(j-c)}$	IGBT	-	-	0.27	$^\circ\text{C/W}$
		FWD	-	-	0.48	
Contact thermal resistance (1device) (*1)	$R_{th(c-f)}$	with thermal compound	-	0.050	-	

(\*1) This is the value which is defined mounting on the additional cooling fin with thermal compound.

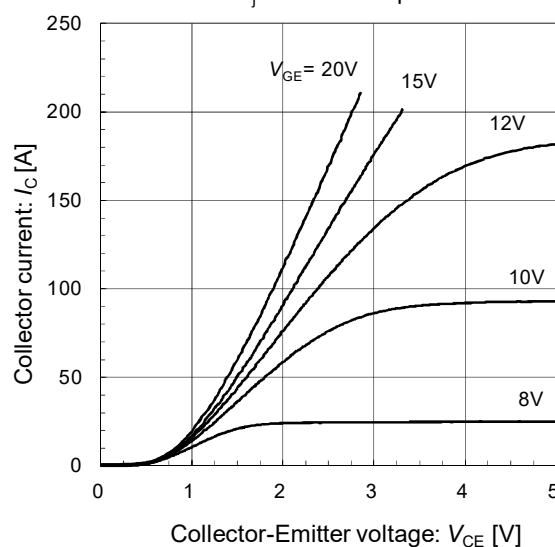
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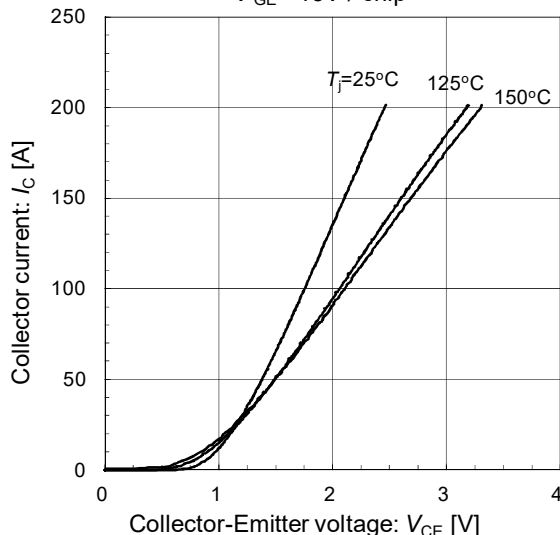
Collector current vs. Collector-Emittor voltage (typ.)  
 $T_j = 25^\circ\text{C}$  / chip



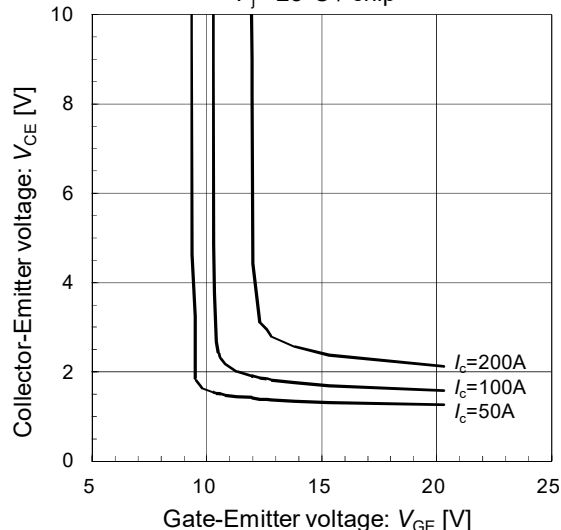
Collector current vs. Collector-Emittor voltage (typ.)  
 $T_j = 150^\circ\text{C}$  / chip



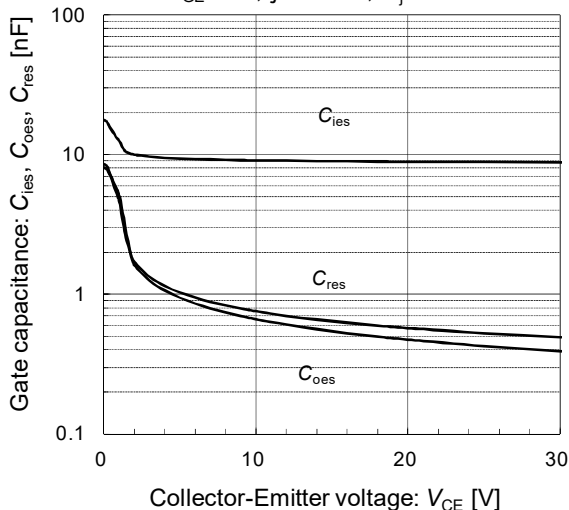
Collector current vs. Collector-Emittor voltage (typ.)  
 $V_{GE} = 15\text{V}$  / chip



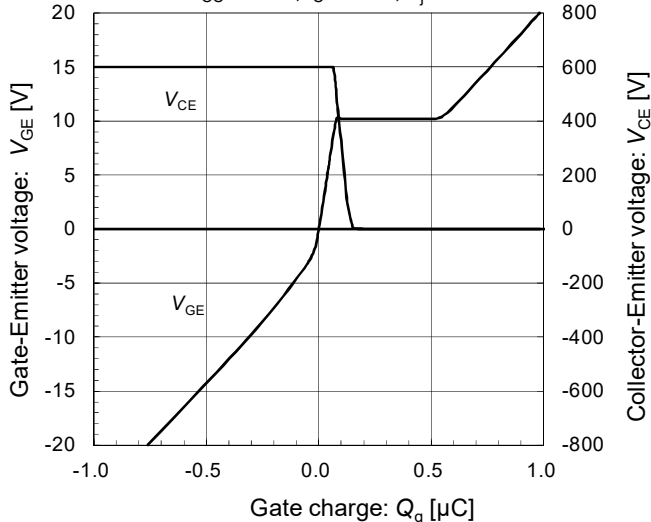
Collector-Emittor voltage vs. Gate-Emittor voltage (typ.)  
 $T_j = 25^\circ\text{C}$  / chip



Gate capacitance vs. Collector-Emittor voltage (typ.)  
 $V_{GE} = 0\text{V}$ ,  $f = 1\text{MHz}$ ,  $T_j = 25^\circ\text{C}$



Dynamic gate charge (typ.)  
 $V_{CC} = 600\text{V}$ ,  $I_C = 100\text{A}$ ,  $T_j = 25^\circ\text{C}$

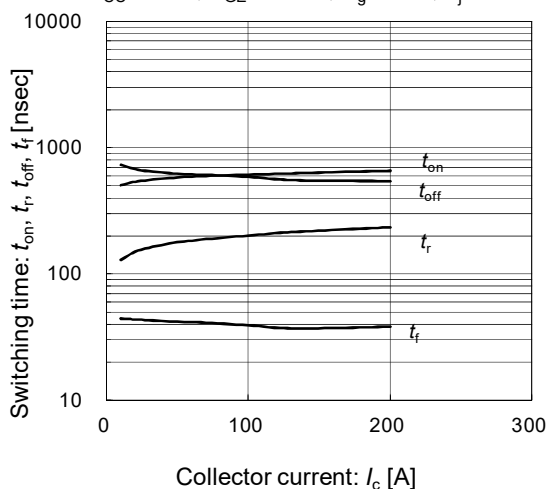


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## IGBT Modules

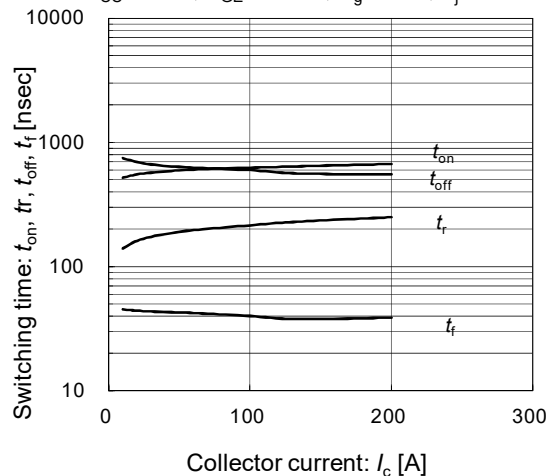
Switching time vs. Collector current (typ.)

$V_{CC}=600V, V_{GE}=\pm 15V, R_g=1.6\Omega, T_j=125^\circ C$



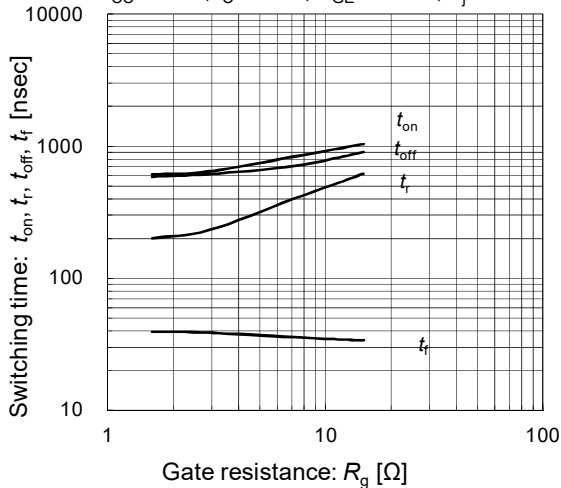
Switching time vs. Collector current (typ.)

$V_{CC}=600V, V_{GE}=\pm 15V, R_g=1.6\Omega, T_j=150^\circ C$



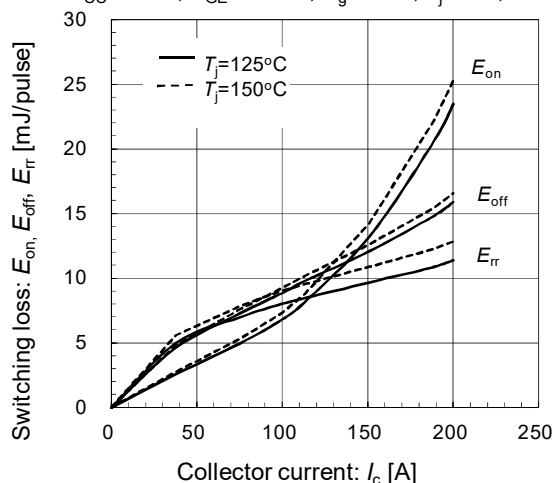
Switching time vs. Gate resistance (typ.)

$V_{CC}=600V, I_c=100A, V_{GE}=\pm 15V, T_j=125^\circ C$



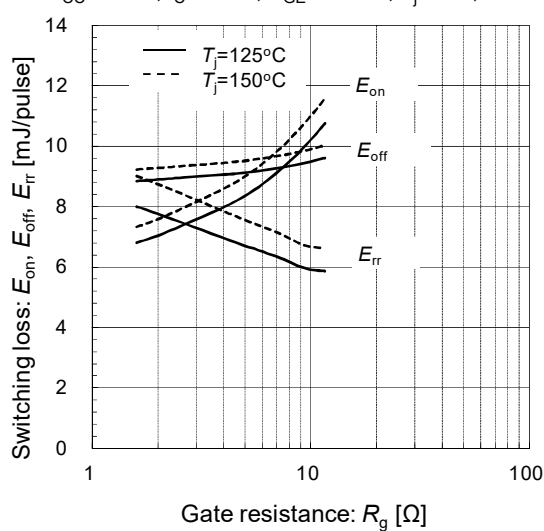
Switching loss vs. Collector current (typ.)

$V_{CC}=600V, V_{GE}=\pm 15V, R_g=1.6\Omega, T_j=125, 150^\circ C$



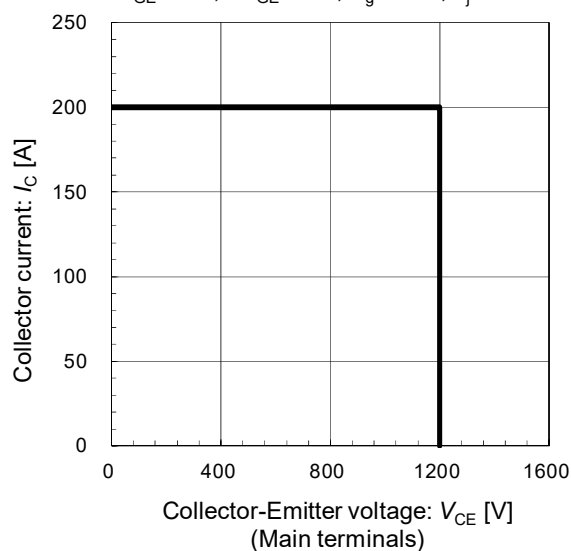
Switching loss vs. Gate resistance (typ.)

$V_{CC}=600V, I_c=100A, V_{GE}=\pm 15V, T_j=125, 150^\circ C$



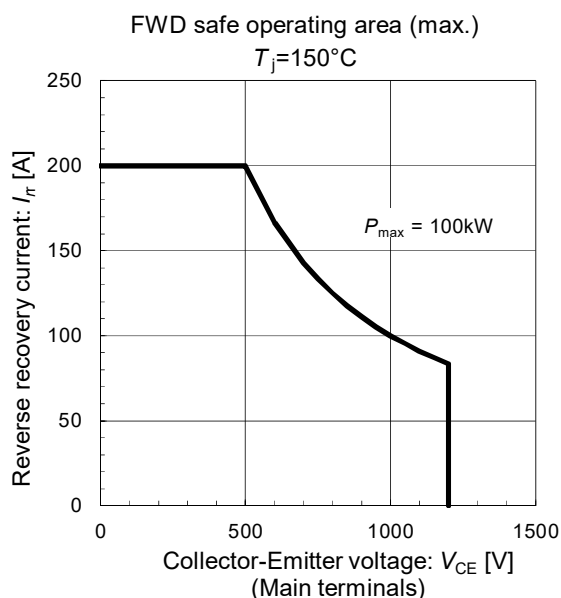
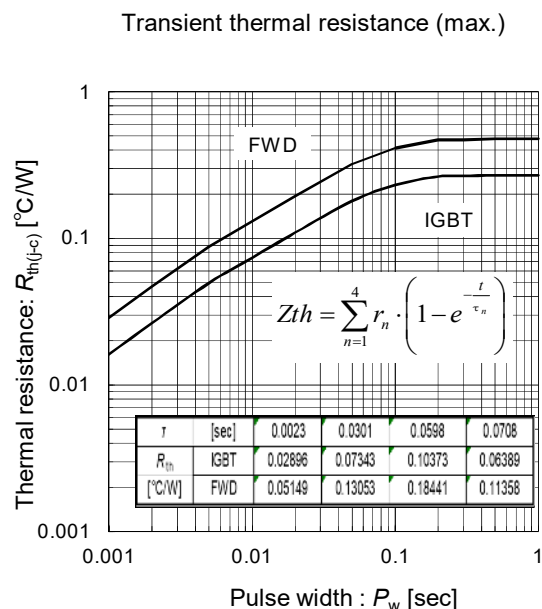
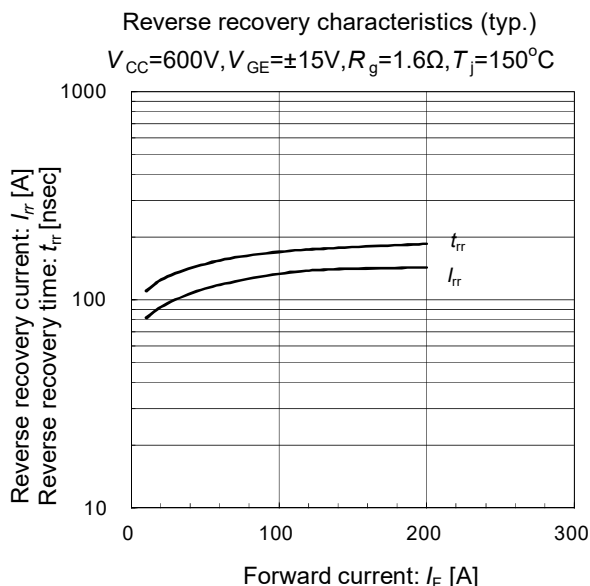
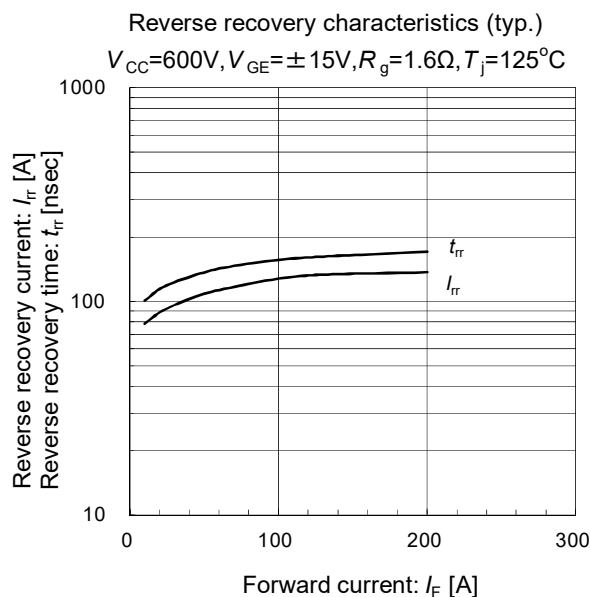
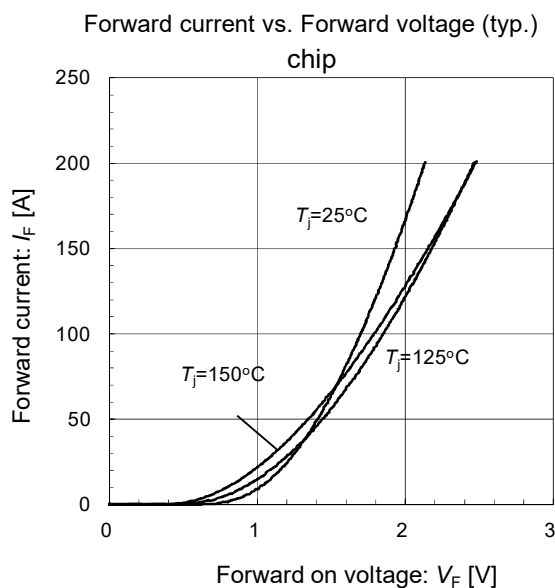
Reverse bias safe operating area (max.)

$+V_{GE}=15V, -V_{GE}=15V, R_g=1.6\Omega, T_j=150^\circ C$



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