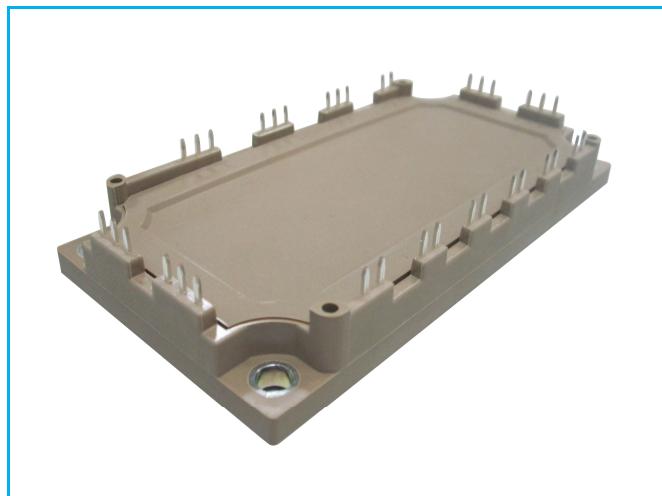


PRODUCT FEATURES

- High level of integration
- IGBT CHIP(Trench+Field Stop technology)
- Low saturation voltage and positive temperature coefficient
- Fast switching and short tail current
- Free wheeling diodes with fast and soft reverse recovery
- Industry standard package with insulated copper base plate and soldering pins for PCB mounting
- Temperature sense included



APPLICATIONS

- AC motor control
- Motion/servo control
- Inverter and power supplies

IGBT-inverter

ABSOLUTE MAXIMUM RATINGS($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter/Test Conditions		Values	Unit
V_{CES}	Collector Emitter Voltage	$T_J=25^\circ\text{C}$	1200	V
V_{GES}	Gate Emitter Voltage		± 20	
I_C	DC Collector Current	$T_c=25^\circ\text{C}, T_{Jmax}=175^\circ\text{C}$	218	A
		$T_c=95^\circ\text{C}, T_{Jmax}=175^\circ\text{C}$	150	
I_{CM}	Repetitive Peak Collector Current	$t_p=1\text{ms}$	300	
P_{tot}	Power Dissipation Per IGBT	$T_c=25^\circ\text{C}, T_{Jmax}=175^\circ\text{C}$	750	W

Diode-inverter

ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter/Test Conditions		Values	Unit
V_{RRM}	Repetitive Reverse Voltage	$T_J=25^\circ\text{C}$	1200	V
$I_{F(AV)}$	Average Forward Current		150	
I_{FRM}	Repetitive Peak Forward Current	$t_p=1\text{ms}$	300	A
I^2t		$T_J=125^\circ\text{C}, t=10\text{ms}, V_R=0\text{V}$	6050	

MacMic Science & Technology Co., Ltd.

Add: #18, Hua Shan Zhong Lu, New District, Changzhou City, Jiangsu Province, P. R .of China

MMG150W120X6TC

IGBT-inverter

ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter/Test Conditions		Min.	Typ.	Max.	Unit	
$V_{GE(\text{th})}$	Gate Emitter Threshold Voltage	$V_{CE}=V_{GE}$, $I_C=6\text{mA}$	5.0	5.8	6.5	V	
$V_{CE(\text{sat})}$	Collector - Emitter Saturation Voltage	$I_C=150\text{A}$, $V_{GE}=15\text{V}$, $T_J=25^\circ\text{C}$		1.85	2.25		
		$I_C=150\text{A}$, $V_{GE}=15\text{V}$, $T_J=125^\circ\text{C}$		2.15			
		$I_C=150\text{A}$, $V_{GE}=15\text{V}$, $T_J=150^\circ\text{C}$		2.2			
I_{CES}	Collector Leakage Current	$V_{CE}=1200\text{V}$, $V_{GE}=0\text{V}$, $T_J=25^\circ\text{C}$			1	mA	
		$V_{CE}=1200\text{V}$, $V_{GE}=0\text{V}$, $T_J=150^\circ\text{C}$			10		
I_{GES}	Gate Leakage Current	$V_{CE}=0\text{V}$, $V_{GE}=\pm 20\text{V}$, $T_J=25^\circ\text{C}$	-400		400	nA	
R_{gint}	Integrated Gate Resistor			4		Ω	
Q_g	Gate Charge	$V_{CE}=600\text{V}$, $I_C=150\text{A}$, $V_{GE}=15\text{V}$		0.75		μC	
C_{ies}	Input Capacitance	$V_{CE}=25\text{V}$, $V_{GE}=0\text{V}$, $f=1\text{MHz}$		10.5		nF	
C_{res}	Reverse Transfer Capacitance			500		pF	
$t_{d(\text{on})}$	Turn on Delay Time	$V_{CC}=600\text{V}$, $I_C=150\text{A}$, $R_G=3.6\Omega$, $V_{GE}=\pm 15\text{V}$, Inductive Load	$T_J=25^\circ\text{C}$		100	ns	
			$T_J=125^\circ\text{C}$		120	ns	
			$T_J=150^\circ\text{C}$		130	ns	
t_r	Rise Time		$T_J=25^\circ\text{C}$		52	ns	
			$T_J=125^\circ\text{C}$		56	ns	
			$T_J=150^\circ\text{C}$		58	ns	
$t_{d(\text{off})}$	Turn off Delay Time	$V_{CC}=600\text{V}$, $I_C=150\text{A}$, $R_G=3.6\Omega$, $V_{GE}=\pm 15\text{V}$, Inductive Load	$T_J=25^\circ\text{C}$		350	ns	
			$T_J=125^\circ\text{C}$		410	ns	
			$T_J=150^\circ\text{C}$		430	ns	
t_f	Fall Time		$T_J=25^\circ\text{C}$		120	ns	
			$T_J=125^\circ\text{C}$		180	ns	
			$T_J=150^\circ\text{C}$		200	ns	
E_{on}	Turn on Energy	$V_{CC}=600\text{V}$, $I_C=150\text{A}$, $R_G=3.6\Omega$, $V_{GE}=\pm 15\text{V}$, Inductive Load	$T_J=125^\circ\text{C}$		22.4	mJ	
			$T_J=150^\circ\text{C}$		24.9	mJ	
E_{off}	Turn off Energy		$T_J=125^\circ\text{C}$		13.3	mJ	
			$T_J=150^\circ\text{C}$		14.2	mJ	
I_{sc}	Short Circuit Current	$tpsc \leq 10\mu\text{s}$, $V_{GE}=15\text{V}$, $T_J=125^\circ\text{C}$, $V_{CC}=800\text{V}$		560		A	
R_{thJC}	Junction to Case Thermal Resistance (Per IGBT)				0.2	K/W	

Diode-inverter

ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter/Test Conditions		Min.	Typ.	Max.	Unit
V_F	Forward Voltage	$I_F=150\text{A}$, $V_{GE}=0\text{V}$, $T_J=25^\circ\text{C}$		1.8	2.3	V
		$I_F=150\text{A}$, $V_{GE}=0\text{V}$, $T_J=125^\circ\text{C}$		1.55		
		$I_F=150\text{A}$, $V_{GE}=0\text{V}$, $T_J=150^\circ\text{C}$		1.5		
t_{rr}	Reverse Recovery Time	$I_F=150\text{A}$, $V_R=600\text{V}$, $dI_F/dt=-2500\text{A}/\mu\text{s}$, $T_J=150^\circ\text{C}$		480		ns
I_{RRM}	Max. Reverse Recovery Current			168		A
Q_{RR}	Reverse Recovery Charge	$T_J=150^\circ\text{C}$		33		μC
E_{rec}	Reverse Recovery Energy			11.7		mJ
R_{thJCD}	Junction to Case Thermal Resistance (Per Diode)			0.36	K/W	

MMG150W120X6TC

NTC CHARACTERISTICS ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter/Test Conditions		Min.	Typ.	Max.	Unit
R_{25}	Resistance	$T_c = 25^\circ\text{C}$		5		$\text{k}\Omega$
$B_{25/50}$	$R_2 = R_{25} \exp [B_{25/50}(1/T_2 - 1/(298.15 \text{ K}))]$			3375		K

MODULE CHARACTERISTICS ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter/Test Conditions	Values	Unit
T_{Jmax}	Max. Junction Temperature	175	$^\circ\text{C}$
T_{Jop}	Operating Temperature	-40~150	
T_{stg}	Storage Temperature	-40~125	
V_{isol}	Isolation Breakdown Voltage AC, 50Hz(R.M.S), t=1minute	3000	V
CTI	Comparative Tracking Index	> 200	
Md	Mounting Torque Recommended (M5)	2.5~5	Nm
Weight		300	g

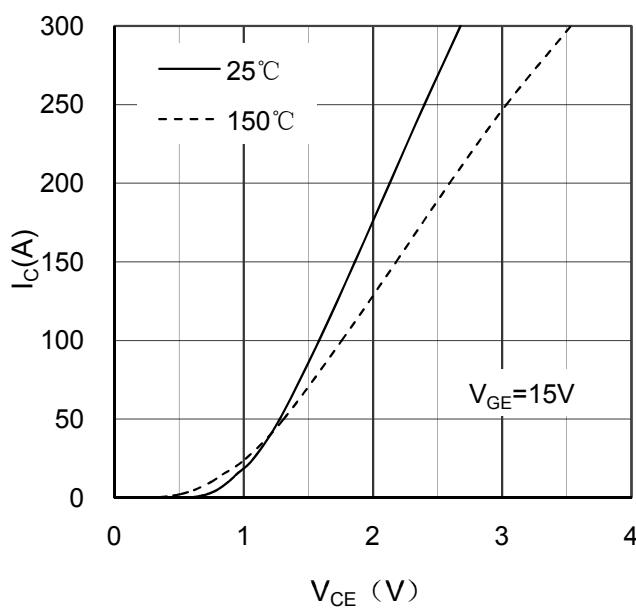


Figure 1. Typical Output Characteristics IGBT-inverter

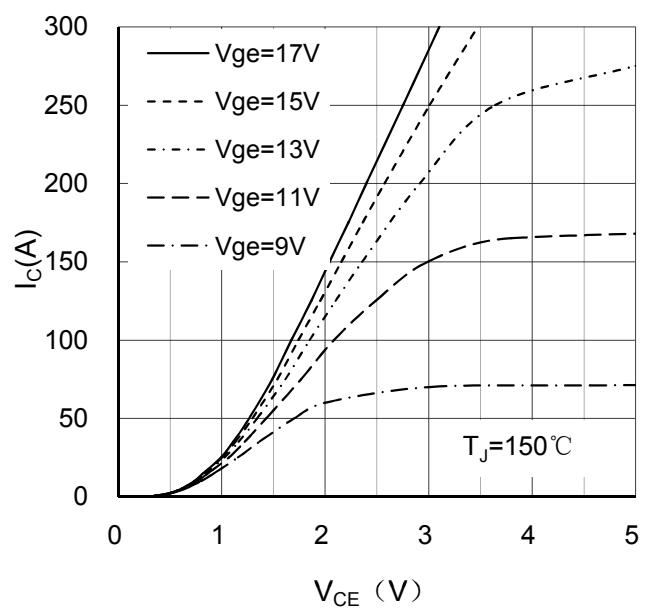


Figure 2. Typical Output Characteristics IGBT-inverter

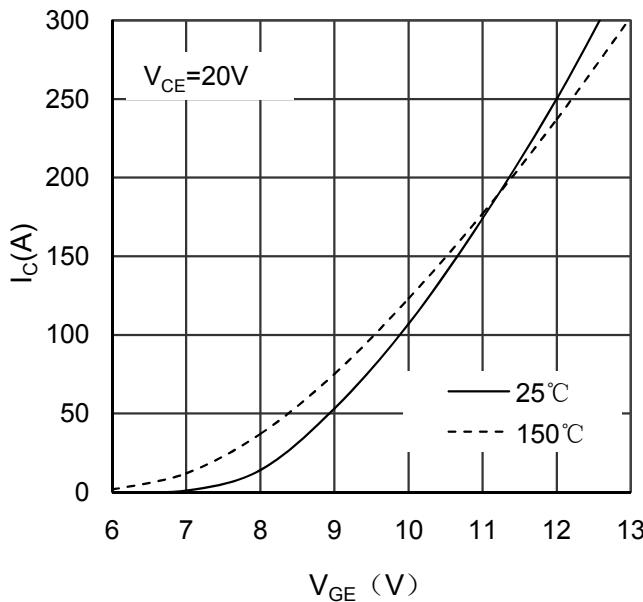


Figure 3. Typical Transfer characteristics IGBT-inverter

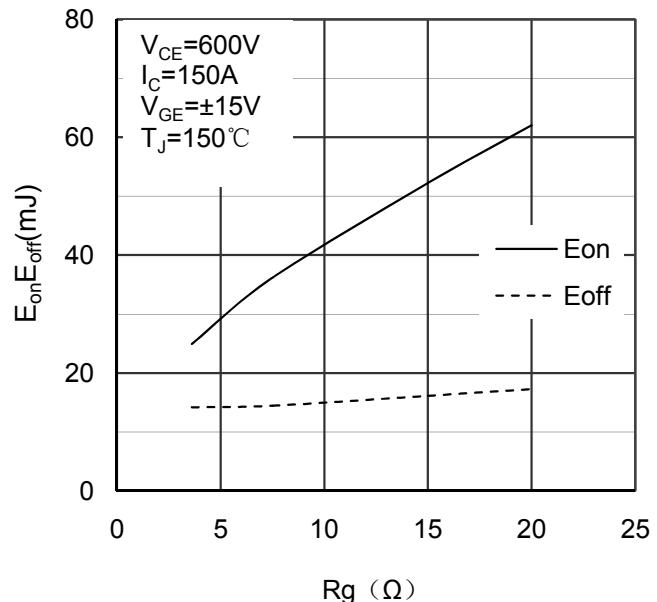


Figure 4. Switching Energy vs Gate Resistor IGBT-inverter

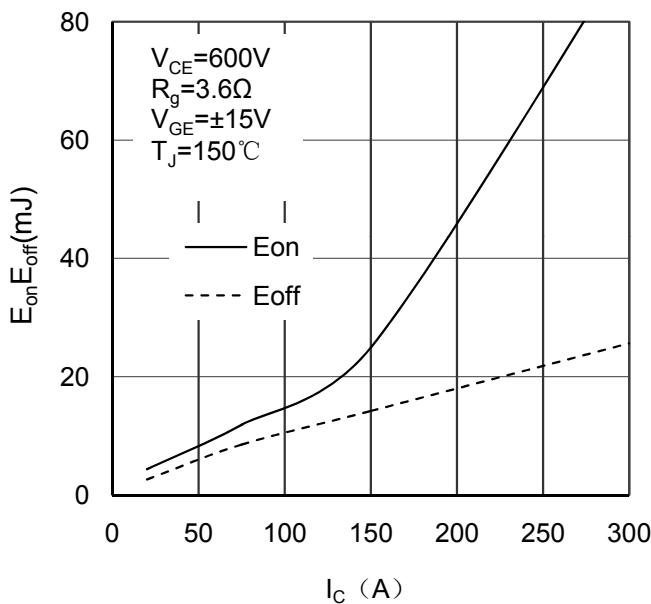


Figure 5. Switching Energy vs Collector Current IGBT-inverter

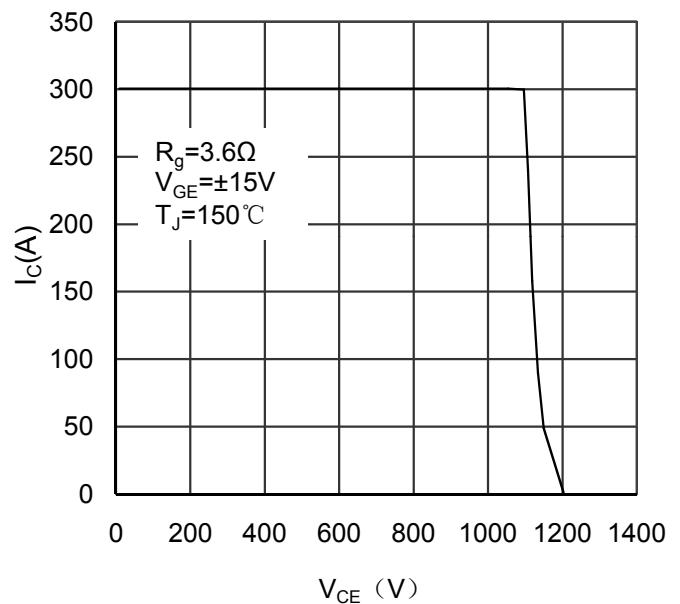


Figure 6. Reverse Biased Safe Operating Area IGBT-inverter

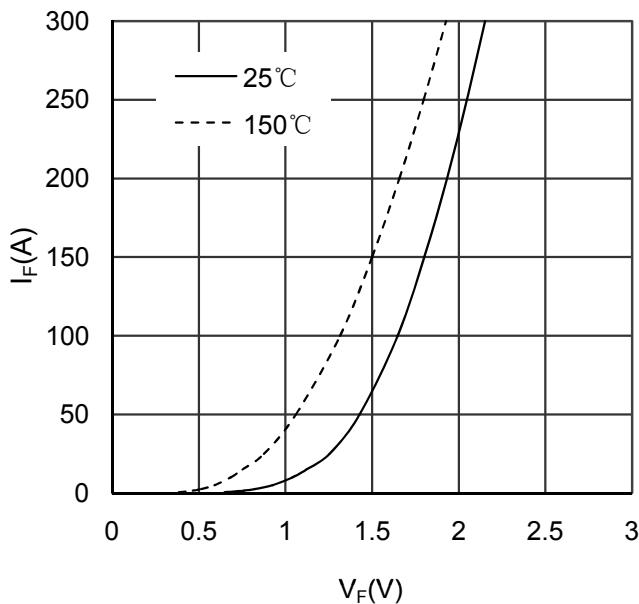


Figure 7. Diode Forward Characteristics Diode -inverter

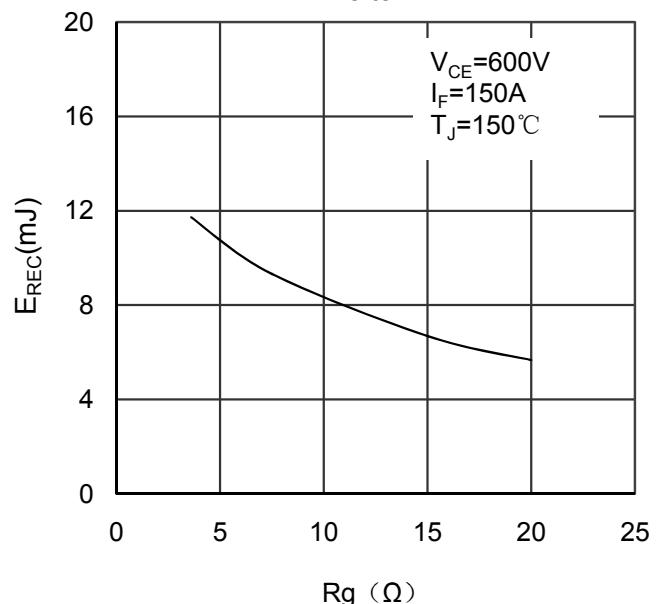


Figure 8. Switching Energy vs Gate Resistor Diode -inverter

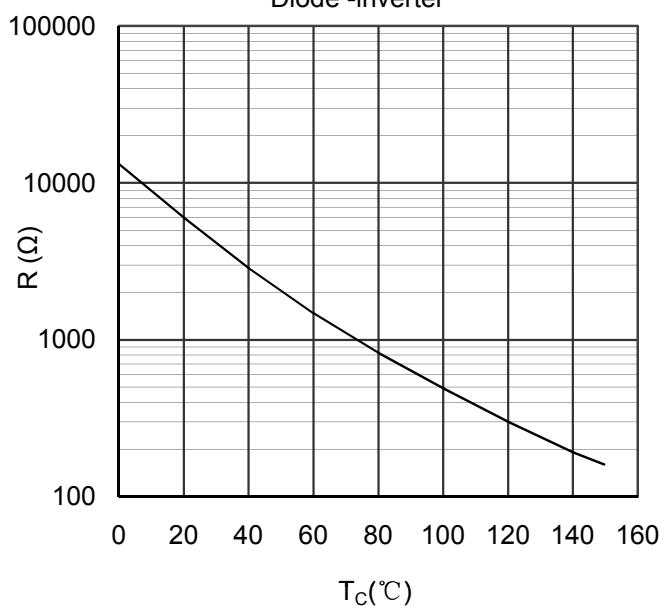
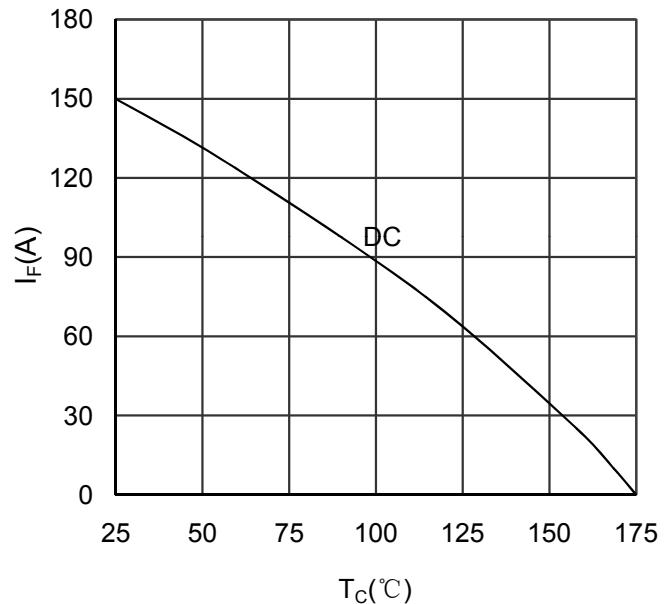
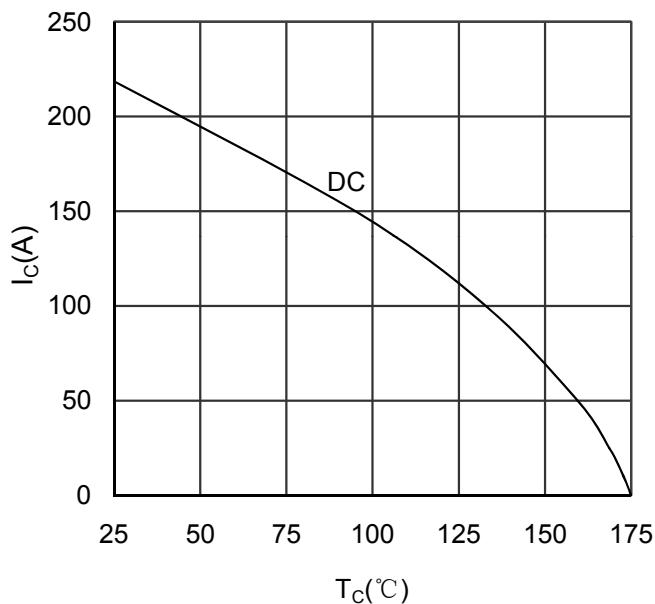
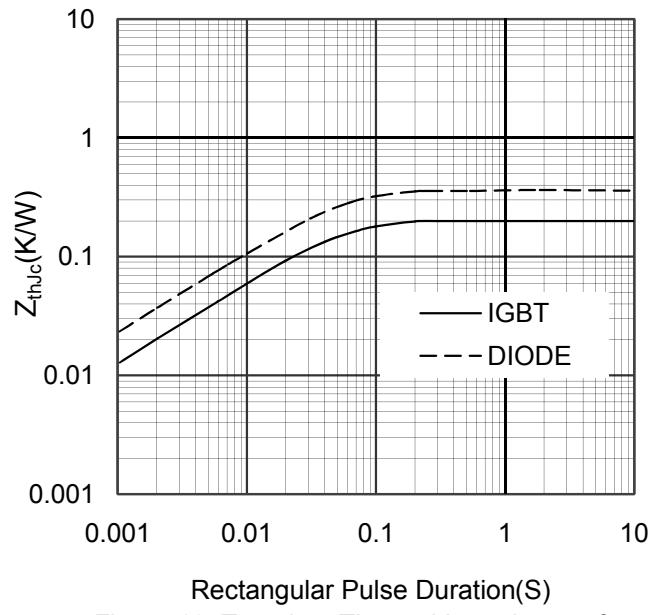
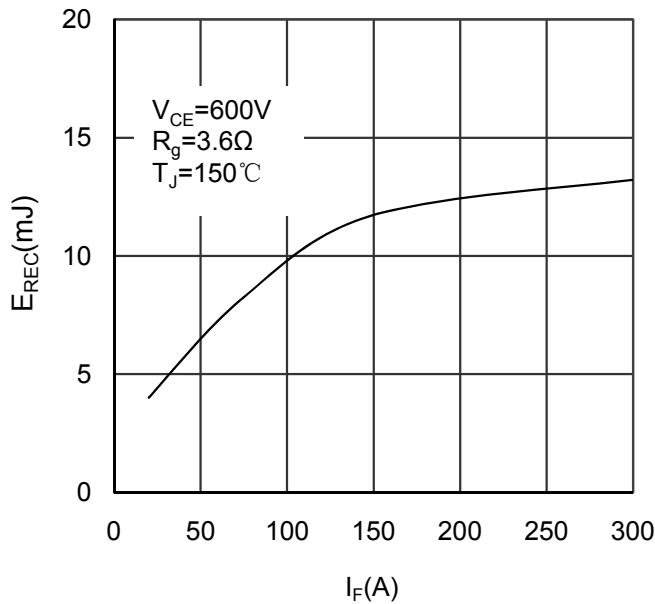


Figure 13. NTC Characteristics

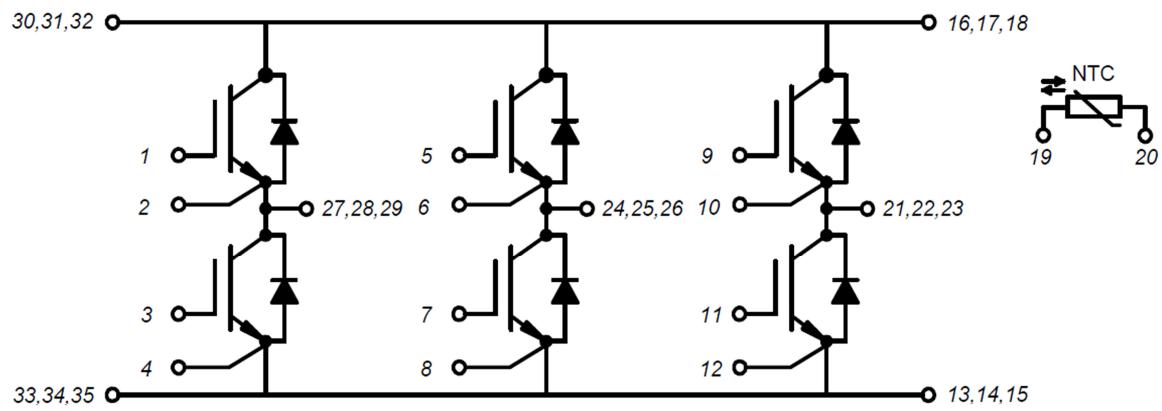
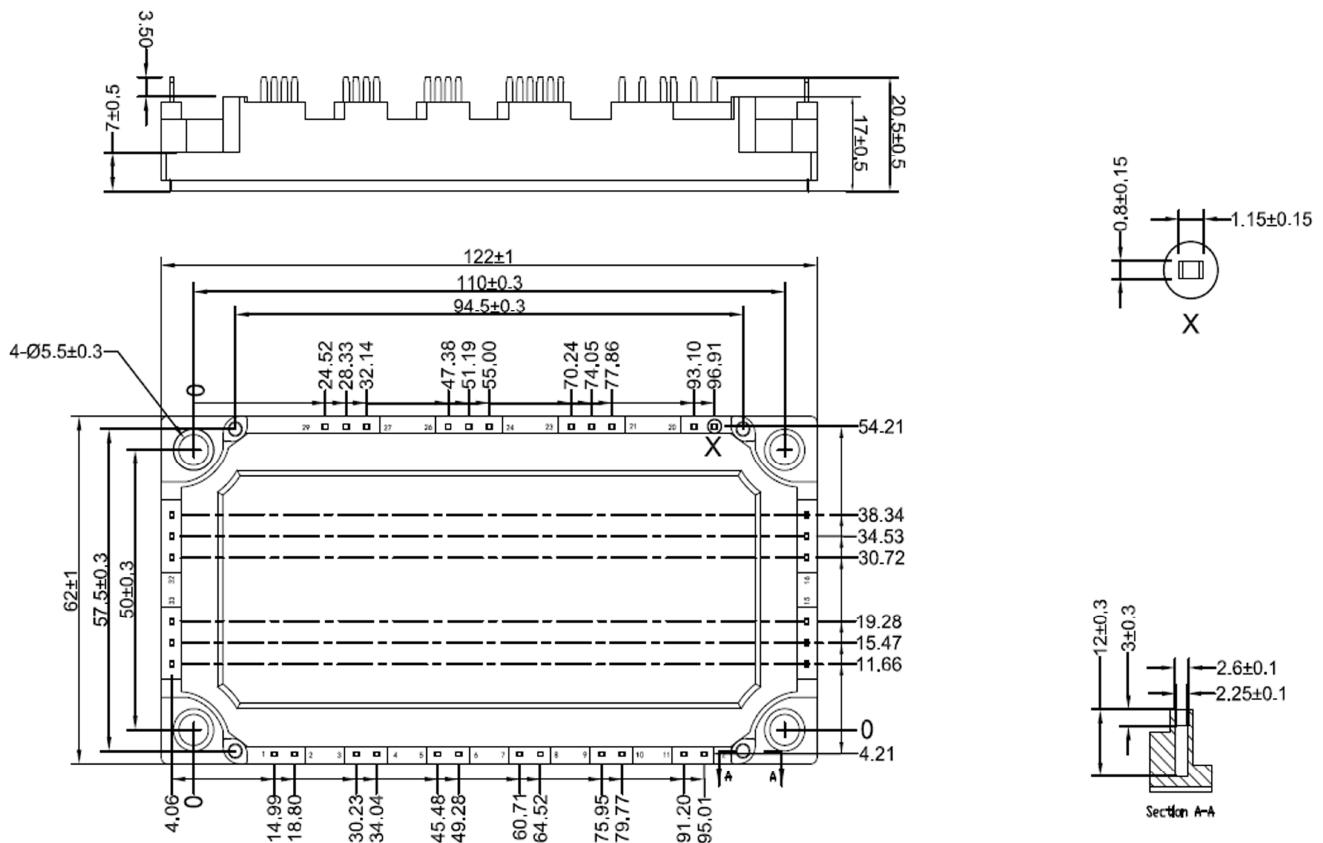


Figure 14. Circuit Diagram



Dimensions in (mm)

Figure 15. Package Outline